

Correction

RESPONSE

THE FOLLOWING IS A COMPLETE LIST OF CLAIMS THAT APPLICANTS
ELECT TO PROSECUTE:

1. A control circuit comprising:
a comparator having a first input for receiving an input signal and an output for providing an output signal;
a reference voltage source coupled to a comparator second input; and
a feedback network coupled to the second input for providing a hysteresis window.
2. The control circuit of claim 1, wherein the comparator is a high speed voltage comparator.
3. The control circuit of claim 1, wherein the input signal comprises an output signal of an IEEE 1394 compliant physical layer output driver circuit.
4. The control circuit of claim 1, wherein the output signal is coupled to a glass fiber optical physical medium dependent sub-layer.

claims 5-32 cancel.